L Number	Hits	Search Text	DB	Time stamp	\neg
1	46	controller same (activate adj keys)	USPAT;	2003/06/19 11:0	8
2	1	20030032486.pn. and controller	US-PGPUB USPAT; US-PGPUB	2003/06/19 11:4	7
3	1	20030032486.pn. and decoder	USPAT; US-PGPUB	2003/06/19 11:4	7
_	1	packing adj unpacking adj (ic or	USPAT; US-PGPUB	2003/01/27 11:1	.6
-	1	<pre>(integrated adj circuit)) updating adj data adj (ic or (integrated adj circuit))</pre>	USPAT; US-PGPUB	2003/01/27 09:5	8
-	2	(structure or device or apparatus) adj (packing or unpacking) adj (ic or	USPAT; US-PGPUB	2003/01/27 10:0	6
-	1	(integrated adj circuit)) (upper adj plate) and (coding adj buttons) and (pcb or (printed adj circuit adj	USPAT; US-PGPUB	2003/01/27 10:0	8
-	2973	board)) decoder and controller and (pcb or	USPAT;	2003/06/19 11:0	18
_ =	2015	(printed adj circuit adj board)) (decoder and controller and (pcb or (printed adj circuit adj board))) and (ic	US-PGPUB USPAT; US-PGPUB	2003/01/27 10:1	9
-	2015	or (integrated adj circuit)) ((decoder and controller and (pcb or (printed adj circuit adj board))) and (ic or (integrated adj circuit))) and (pcb or	USPAT; US-PGPUB	2003/01/27 10:2	2
_	1	<pre>(printed adj circuit adj board)) (((decoder and controller and (pcb or (printed adj circuit adj board))) and (ic or (integrated adj circuit))) and (pcb or (printed adj circuit adj board))) and</pre>	USPAT; US-PGPUB	2003/01/27 10:2	:3
-	25	<pre>(coding adj buttons) (((decoder and controller and (pcb or (printed adj circuit adj board))) and (ic or (integrated adj circuit))) and (pcb or (printed adj circuit adj board))) and</pre>	USPAT; US-PGPUB	2003/01/27 10:2	:3
-	820	trough (((decoder and controller and (pcb or (printed adj circuit adj board))) and (ic or (integrated adj circuit))) and (pcb or	USPAT; US-PGPUB	2003/01/27 11:1	.3
-	18	<pre>(printed adj circuit adj board))) and keys (((decoder and controller and (pcb or (printed adj circuit adj board))) and (ic or (integrated adj circuit))) and (pcb or (printed adj circuit adj board))) and</pre>	USPAT; US-PGPUB	2003/01/27 11:1	.4
-	14	<pre>(upper adj plate) ((((decoder and controller and (pcb or (printed adj circuit adj board))) and (ic or (integrated adj circuit))) and (pcb or (printed adj circuit adj board))) and</pre>	USPAT; US-PGPUB	2003/01/27 11:1	4
-	3	<pre>(upper adj plate)) and (lower adj plate) method adj (packing or unpacking) adj (ic or (integrated adj circuit))</pre>	USPAT; US-PGPUB	2003/01/28 12:2	27
	0	5705857.pn. and damascene	USPAT; US-PGPUB	2003/06/14 10:1	.9
_	1	5705857.pn. and variability	USPAT; US-PGPUB	2003/06/19 11:4	.6
_	1	5777433.pn.	USPAT; US-PGPUB	2003/01/29 09:4	.1
_	1	5298768.pn.	USPAT; US-PGPUB	2003/01/29 09:4	2
-	1	6355946.pn.	USPAT; US-PGPUB	2003/01/29 09:4	3
-	1	5959316.pn.	USPAT; US-PGPUB	2003/01/29 09:4	
-	1	6407411.pn.	USPAT; US-PGPUB	2003/01/29 09:4	
-	1	6069440.pn.	USPAT; US-PGPUB	2003/06/14 10:2	
_	1	6045240.pn.	USPAT; US-PGPUB	2003/01/29 09:4	4

-	0	5705857.pn. and damascene	USPAT; US-PGPUB	2003/06/14 10:19
-	1765	(keys or buttons) and (pcb or (printed adj circuit adj board)) and decoder and controller	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2003/06/14 10:24
_	o	(keys or buttons) and (pcb or (printed adj circuit adj board)) and decoder and controller and (packing adj (ic or (integrated adj circuit)))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2003/06/14 10:25
		(keys or buttons) and (pcb or (printed adj circuit adj board)) and decoder and controller and (unpacking adj (ic or (integrated adj circuit)))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2003/06/14 10:27
-	0	(keys or buttons) and (pcb or (printed adj circuit adj board)) and decoder and controller and (((preserving or updating) adj data) adj (ic or (integrated adj circuit)))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2003/06/14 10:28
	2	(((preserving or updating) adj data) adj (ic or (integrated adj circuit)))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2003/06/14 10:39
_	0	safe adj data adj updating adj process	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2003/06/14 10:35
-	0	update adj data adj safely	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2003/06/14 10:35
-	0	prevent adj theft adj data adj (ic or (integrated adj circuit))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2003/06/14 10:36
-	0	prevent adj copying adj data adj (ic or (integrated adj circuit))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2003/06/14 10:36
_	0	<pre>prevent adj duplication adj data adj (ic or (integrated adj circuit))</pre>	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2003/06/14 10:39
_ •	0	prevent adj duplication adj data adj ic	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2003/06/14 10:39
_	1	((preserving or updating) adj data) adj ic	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2003/06/14 10:41
_	0	prevent adj ic adj theft	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2003/06/14 10:42

-	0	prevent adj ic adj stolen	USPAT;	2003/06/14 10:42
			US-PGPUB; EPO; JPO;	
			DERWENT;	
			IBM TDB	
	0	stop adj ic adj stolen	USPAT;	2003/06/14 10:45
_		scop adj ic adj scoren	US-PGPUB;	2003/00/14 10:43
			EPO; JPO;	
			DERWENT;	
			IBM TDB	
_	32	encasing adj (pcb or (printed adj circuit	USPAT;	2003/06/18 16:04
		adj board))	US-PGPUB;	
			EPO; JPO;	
			DERWENT;	
			IBM_TDB	
-	409	securing adj (pcb or (printed adj circuit	USPAT;	2003/06/14 10:50
		adj board))	US-PGPUB;	
			EPO; JPO;	
			DERWENT;	
	1	/comprise add /mah or /printed add giravit	IBM_TDB USPAT;	2003/06/14 10:52
-	1	(====================================	USPAT; US-PGPUB;	2003/00/14 10:32
		adj board))) and theft	EPO; JPO;	
			DERWENT;	
			IBM TDB	
_	0	(securing adj (pcb or (printed adj circuit	USPAT;	2003/06/14 10:52
		adj board))) and stealing	US-PGPUB;	
			EPO; JPO;	
			DERWENT;	
			IBM_TDB	
-	3	(securing adj (pcb or (printed adj circuit	USPAT;	2003/06/14 10:54
		adj board))) and copying	US-PGPUB;	
			EPO; JPO;	
			DERWENT;	
	0	 protect adj program adj stored adj ic	IBM_TDB USPAT;	2003/06/14 10:54
-		protect adj program adj stored adj ic	US-PGPUB;	2003/00/14 10:54
			EPO; JPO;	
			DERWENT;	
***************************************			IBM TDB	
-	0	protect adj program adj ic	USPĀT;	2003/06/14 11:08
		⁻	US-PGPUB;	
			EPO; JPO;	
			DERWENT;	
			IBM_TDB	0000/05/11 11 00
-	2	unpacking adj encased adj pcb	USPAT;	2003/06/14 11:09,
			US-PGPUB;	
	1		EPO; JPO; DERWENT;	
			IBM TDB	
_	52	pc adj card adj housing	USPAT;	2003/06/18 16:11
		po day cara day nodorny	US-PGPUB;	
			EPO; JPO;	
			DERWENT;	
			IBM_TDB	
-	78	(pcb or (printed adj circuit adj board))	USPĀT;	2003/06/18 16:12
		adj protection	US-PGPUB;	
			EPO; JPO;	
			DERWENT;	
L			IBM_TDB	